

Substitute for Form 1449/PTO <b>INFORMATION DISCLOSURE</b> <b>STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>				<b>Complete if Known</b>	
				Application Number	09/884,718
				Filing Date	June 18, 2001
				First Named Inventor:	Edward T. Grochowski
				Art Unit	2183
				Examiner Name	William M. Treat
				Attorney Docket Number	042390.P4900D
<b>NON PATENT LITERATURE DOCUMENTS</b>					
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T <sup>2</sup>
WMT		DULONG, ET AL., "An Overview of the Intel® IA-64 Compiler", Intel Technology Journal Q4, 1999, Pages 1-15.			
↓		Intel® Itanium™ Architecture Software Developer's Manual, Part II: Optimization Guide for the Intel® Itanium™ Architecture, Predication, Control Flow, and Instruction Stream, Volume 1: Application Architecture, Revision 2.0, December 2001, Pages 1:129, 1:155 – 1:163.			
		Intel® Itanium™ Processor, Product Highlights, Copyright © 2001, Intel Corporation.			
		TOM R. HALFHILL, "Beyond Pentium II", BYTE.com, December 1997, Pages 1-10.			
		JIM TURLEY, "64-Bit CPUs: What you Need to Know", Extreme Tech, February 8, 2002, Pages 1-26.			
		NICHOLAS P. CARTER, ECE 412-Advanced Computer Architecture, Lecture 4: Predication, September 13, 2000, Pages 1-17.			
		AUGUST ET AL., "Architectural Support for Compiler-Synthesized Dynamic Branch Prediction Strategies: Rationale and Initial Results", Third International Symposium on High-Performance Computer Architecture, IEEE, Feb. 1-5, 1997, pp. 84-93.			
		PNEVMATIKATOS ET AL., "Guarded Execution and Branch Prediction in Dynamic ILP Processors", Proceedings of the 21 <sup>st</sup> Annual International Symposium on Computer Architecture, IEEE, pp. 120-129.			
WMT		MAHLKE ET AL., "Characterizing the Impact of Predicated Execution Branch Prediction", Proceedings of the 27 <sup>th</sup> Annual International Symposium Microarchitecture, Micro-27, IEEE, Nov. 30-Dec. 2, 1994, pp. 217-227.			

Examiner Signature	W. TREAT	Date Considered	3/29/05
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\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>Applicant is to place a check mark here if English Translation is attached.  
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<b>Form PTO-1449 (Modified)</b>		<b>Atty Docket No.:</b> 62890-24900		<b>Serial No.:</b> Unknown			
<b>List of Patents and Publications Statement</b> (Use several sheets if necessary)				<b>Applicant:</b> Edward T. Grochowski et al.			
				<b>Filing Date:</b> June 18, 2001			
<b>REFERENCE DESIGNATION</b>			<b>U.S. PATENT DOCUMENTS</b>				
<b>Examiner Initials</b>		<b>Document No.</b>		<b>Class</b>	<b>Sub-Class</b>		
WMT ↓ WMT	AA	4,578,750	Amdahl et al.	364	200		
	AB	4,967,351	Zmyslowski et al.	364	200		
	AC	4,999,800	Birger	364	748		
	AD	5,471,593	Branigin	395	375		
	AE	6,021,487	Maliszewski	712	221		
	AF						
	AG						
	AH						
	AI						
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	AK						
	AL						
	AM						
	AN						
	AO						
	AP						
	<b>FOREIGN PATENT DOCUMENTS</b>						
<b>No.</b>		<b>Document No.</b>	<b>Date</b>	<b>Country</b>	<b>Class</b>	<b>Sub-Class</b>	<b>Translation</b>
WMT	AQ	EP 0 855 645 A2	7/29/98	EP	G06F	9/38	
↓	AR	WO 97/48042	12/18/97	PCT	G06F	9/38	
	AS	WO 98/08160	2/26/98	PCT	G06F	9/00, 9/06	
WMT	AT	WO 99/14667	3/25/99	PCT	G06F	9/38	
	AU						
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
WMT	AV	D. Grunwald et al., "Confidence Estimation for Speculation Control", 1998 IEEE, pp. 122 - 131.					
↓	AW	E. Jacobsen et al., "Assigning Confidence Branch Predictions", 1996 IEEE, pp. 142 - 152.					
	AX	S. Manne et al., "Pipeline Gating: Speculation Control for Energy Reduction", 1998 IEEE, pp. 132 - 141.					
	AY	K.C. Tal et al., "Evaluation of a predicate-based software testing strategy", IBM Systems Journal, Vol. 33, No. 3, 1994, pp. 445-457.					
WMT	AZ	S. Mahlke et al., "Effective Compiler Support for Predicated Execution Using the Hyperblock", 1992 IEEE, pp. 45-54.					
<b>Examiner</b>			<b>Date Considered</b>				
W. T. REAT			6/27/04				
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These documents were already considered and were part of a 1449 mailed back to applicants on 6/30/04.